

Process Change Notice #1201111

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PCN Date: 11Jan12			Effective Date: 17Apr12		
Title: Si3452 Si3453 Firmware Revision					
Originator: John Wilson	ginator: John Wilson Phone: 512-532-5		Dept: Wireline Marketing		
Customer Contact: Kathy Haggar Phone: 512-		261	Dept: Sales		
PCN Type:					
☐ Assembly ☐ Discont	inuance 🗌 P	ackage	e ☐ Test		
☐ Datasheet ☐ Fabrica	orication 🖂 Pi		Revision		
☐ Packing ☐ Labelin	g 🗆 L	ocation	n 🗌 Other		
Last Order Date: 17Apr12					
PCN Details					
Description of Change:					
Silicon Laboratories, Inc (Silicon Labs) is pleased to announce the release of a new firmware revision for the Si3452 and Si3453 Quad High-Voltage Port Controllers for PoE and PoE+ PSEs. This firmware revision improves compliance and addresses several minor flaws. The firmware revision 2.81 includes these changes:					
 Fixed a signed/unsigned calculation error in the routine that measures FET voltage. Presently, parts are being screened for this issue, so the firmware change is transparent to the end-user. Increased the amount of time that the I2C clock line can be held low before the Si3452 aborts the I2C transaction from 5 ms to 30 ms, allowing increased flexibility for the system designer. Fixed an error that occurred in rare cases when reading port current or Vee; there were occasions where the upper byte and the lower byte do not belong to the port reading, because an I2C interrupt occurred between the writing of the two bytes. This error was fixed by disabling interrupts while writing these 2-byte values. Reduced the initialization time from 100msec max to 20msec max, resulting in faster controller readiness. Fixed an issue with current limiting during Tstart in 2x mode, improving "Sifos" PoE test set compliance. Fixed an issue related to the disconnect timing for overloads that occur during the Tstart time period, improving "Sifos" PoE test set compliance. 					
Reason for Change:					
Implementing these changes improves PoE system performance and PoE test set compliance for the product.					



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Impact on Form, Fit, Function, Quality, Reliability:

There is no impact on form, fit, function, quality or reliability. This firmware revision improves compliance and addresses several minor flaws. Firmware revision 2.81 is software backwards compatible with the previous revision.

Product Identification:

Products ordered under the following ordering part numbers are affected by this change:

EXISTING OPN's:

Si3452-B01-GM	Si3453-B01-GM
Si3452A-B01-GM	Si3453A-B01-GM
Si3452B-B01-GM	Si3453B-B01-GM
Si3452C-B01-GM	Si3453C-B01-GM
Si3452D-B01-GM	Si3453D-B01-GM

NEW OPN's:

Si3452-B02-GM	Si3453-B02-GM
Si3452A-B02-GM	Si3453A-B02-GM
Si3452B-B02-GM	Si3453B-B02-GM
Si3452C-B02-GM	Si3453C-B02-GM
Si3452D-B02-GM	Si3453D-B02-GM

Note: Affected Part Numbers also include any additional custom parts made from the above listed base parts and tape and reel equivalents ("R" suffix).

Last Date of Unchanged Product: 17Apr12

Qualification Samples:

Available upon request. Please contact your local Silicon Laboratories sales representative to order samples. A list of Silicon Laboratories sales representatives may be found at www.silabs.com

The Si3452 and Si3453 updated datasheets are available at www.silabs.com

Customer Early Acceptance Sign Off:

Customers may approve early PCN acceptance by completing the information below:

Early Acceptance Date:

Name:

Company:

Email your early Acceptance approval to: katherine.haggar@silabs.com

Qualification Data:

Available upon request. Please contact your local Silicon Labs sales representative. A list of Silicon Labs sales representatives is available at www.silabs.com